



# i.MX RT1010 Crossover MCU with Arm<sup>®</sup> Cortex<sup>®</sup>-M7 Core Operating Up to 500 MHz

## i.MX-RT1010

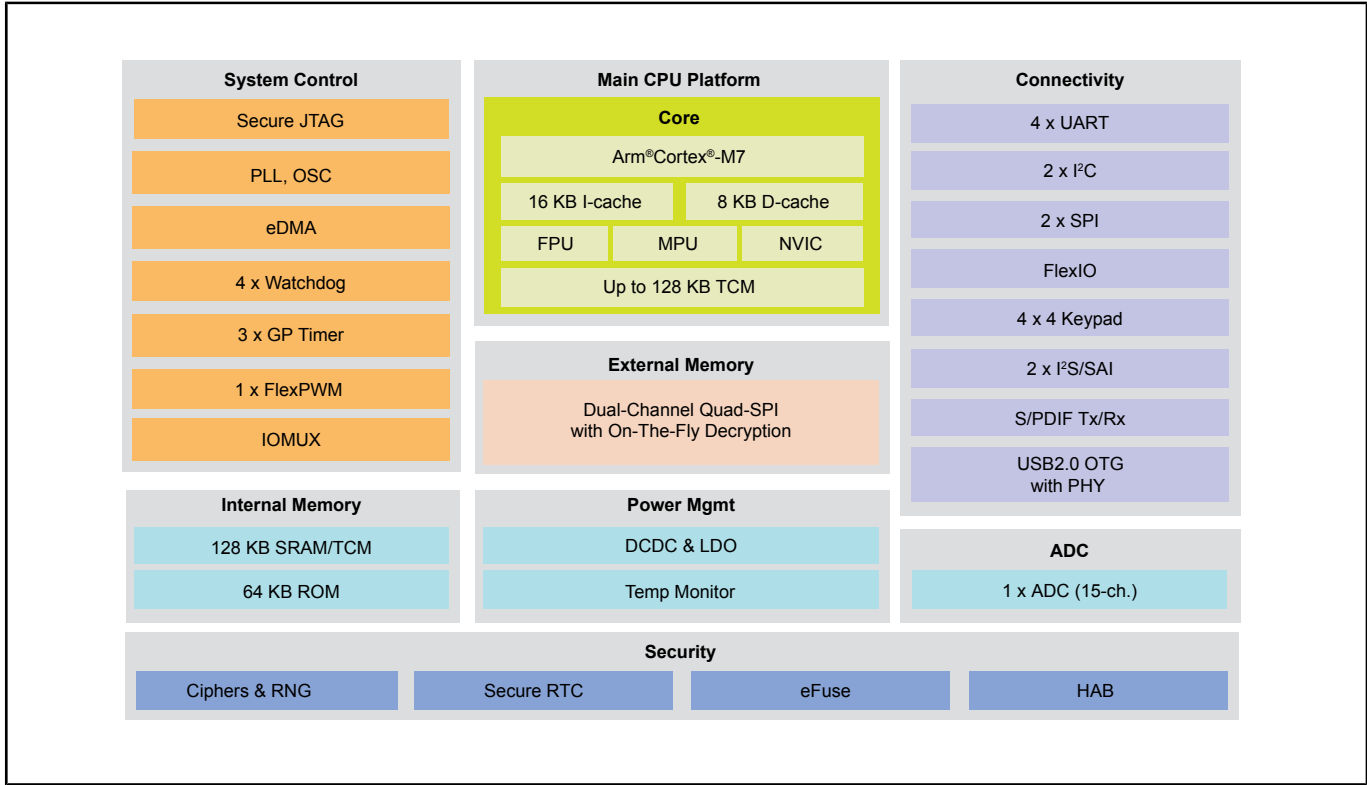
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i.MX RT1010 Crossover MCUs are based on the Arm<sup>®</sup> Cortex<sup>®</sup>-M7 core for real-time performance and high integration for Industrial and IoT applications.

The i.MX RT1010 CM7 operates at up to 500 MHz with 128 KB on-chip RAM that can be configured as TCM or general-purpose. The family offers various memory interfaces and a wide range of connectivity interfaces including UART, SPI, I<sup>2</sup>C, and USB. 80 LQFP packages included for low-cost PCB designs.

The i.MX RT1010 family is supported by the [MCUXpresso ecosystem](#), which includes an SDK, a choice of IDEs and secure provisioning and configuration tools to enable rapid development.

## i.MX RT1010 Crossover MCU Block Diagram



View additional information for [i.MX RT1010 Crossover MCU with Arm® Cortex®-M7 Core Operating Up to 500 MHz.](#)

**Note:** The information on this document is subject to change without notice.

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