

Freescale Semiconductor Addendum

Document Number: MP7410UMAD Rev. 2.1, 05/2009

Errata to MPC7410/MPC7400 RISC Microprocessor Reference Manual, Rev. 2

This errata describes corrections to the MPC7410/MPC7400 RISC Microprocessor Reference Manual, Revision 2. For convenience, the section number and page number of the errata item in the reference manual are provided.

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Section, Page No.

Changes

5.3, 5-19

Modify the fourth paragraph to say the following:

"Thus, multiple BAT hits (with valid bits set) are considered a programming error whether translation is enabled or disabled. This can lead to unpredictable results if translation is enabled, or if translation is disabled when translation is eventually enabled. For the case of unused BATs, if translation is to be enabled, it is a sufficient precaution to simply clear the valid bits of the unused BAT entries."



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Changes

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